INFORMATION DISCLOSURE CITATION

Atty. Docket No.	04329.3268	Application No.	
Applicants	Seiji INUMIYA et al.		
Filing Date	March 12, 2004	Group:	

U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
76	6,020,243	02/01/2000	Wallace et al.			
700						

	•	FOREIGN PATEN	IT DOCUMENT	S,		
	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
24	2000-49349	02/18/2000	Japan			No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)				
7/	S. INUMIYA, et al. "Method of Manufacturing Semiconductor Device", U.S. Application No. 10/101,913, filed March 21, 2002.			

Examiner	Klumper	Date Considered	7/28/08			
*Examiner:	Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					
Form PTO 1449 Pa		Patent and Trademark Office	e - U.S. Department of Commerce			